


PALM INTRANET

 Day : Tuesday
 Date: 1/25/2005
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Inventor Name Search Result

Your Search was:

Last Name = YASAITIS

First Name = JOHN

Application#	Patent#	Status	Date Filed	Title	Inventor Name 16
<u>10735410</u>	Not Issued	030	12/12/2003	GRADED INDEX OPTICAL COUPLER	YASAITIS, JOHN
<u>10656850</u>	Not Issued	020	09/05/2003	LIGHT CONVERSION APPARATUS WITH TOPSIDE ELECTRODE	YASAITIS, JOHN
<u>10259188</u>	Not Issued	071	09/27/2002	FABRICATING COMPLEX MICRO-ELECTROMECHANICAL SYSTEMS USING A FLIP BONDING TECHNIQUE	YASAITIS, JOHN A.
<u>10259174</u>	Not Issued	061	09/27/2002	FABRICATING COMPLEX MICRO-ELECTROMECHANICAL SYSTEMS USING A DUMMY HANDLING SUBSTRATE	YASAITIS, JOHN A.
<u>10259173</u>	Not Issued	083	09/27/2002	FABRICATING COMPLEX MICRO-ELECTROMECHANICAL SYSTEMS USING AN INTERMEDIATE ELECTRODE LAYER	YASAITIS, JOHN A.
<u>07572751</u>	<u>5086370</u>	150	08/24/1990	INTEGRATED CIRCUIT CHIP FORMED WITH A CAPACITOR HAVING A LOW VOLTAGE COEFFICIENT, AND METHOD OF MAKING SUCH CAPACITOR	YASAITIS , JOHN A.
<u>07118451</u>	Not Issued	161	11/09/1987	SEMICONDUCTOR DEVICE FABRICATED WITH PARTIALLY SELF-ALIGNED METAL CONTACT PROCESS	YASAITIS , JOHN A.
<u>06867048</u>	<u>4722910</u>	150	05/27/1986	PARTIALLY SELF-ALIGNED METAL CONTACT PROCESS	YASAITIS , JOHN A.
<u>06860096</u>	<u>4810663</u>	150	05/06/1986	METHOD OF FORMING CONDUCTIVE PATH BY LOW POWER LASER PULSE	YASAITIS , JOHN A.
<u>06657128</u>	<u>4585490</u>	150	10/03/1984	METHOD OF MAKING A CONDUCTIVE PATH IN MULTI-LAYER METAL STRUCTURES BY LOW POWER LASER BEAM	YASAITIS , JOHN A.

<u>06657127</u>	Not Issued	161	10/03/1984	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS FOR MAKING THE SAME	YASAITIS , JOHN A.
<u>06651230</u>	4636404	150	09/17/1984	METHOD AND APPARATUS FOR FORMING LOW RESISTANCE LATERAL LINKS IN A SEMICONDUCTOR DEVICE	YASAITIS , JOHN A.
<u>06389385</u>	Not Issued	161	06/17/1982	METHOD AND APPARATUS FOR FORMING LOW RESISTANCE LATERAL LINKS IN A SEMICONDUCTOR DEVICE	YASAITIS , JOHN A.
<u>06336856</u>	4384299	150	01/04/1982	CAPACITOR MEMORY AND METHODS FOR READING, WRITING, AND FABRICATING CPACITOR MEMORIES	YASAITIS , JOHN A.
<u>06328356</u>	Not Issued	161	12/07/1981	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS FOR MAKING THE SAME	YASAITIS , JOHN A.
<u>06137432</u>	Not Issued	161	04/04/1980	CAPACITOR MEMORY AND METHODS FOR READING, WRITING, AND FABRICATING CAPACITOR MEMORIES	YASAITIS , JOHN A.

Inventor Search Completed: No Records to Display.

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